



The RM5 CCTALK Version Electronic Coin Mechanism ISTRUCTIONS

The identifying code of the RM5 CCTalk electronic coin mechanism is made of 15 digit as the two here after reported RM5GCC24ECC0000 or RM5FCC24ECC0000

The F letter indicates the frontal way out of the rejected coins, whereas the G identifies the up-right position of the rejected coins way out.

The RM5 CCTALK has been conceived specifically for the gambling field La RM5 CCTALK and is featuring at the same time technological innovation and high reliability as witnessed by the 400,000and more units sold on the market.

It exploits the CCTALK communication protocol in order to perfectly satisfy the “incapable of modification” requirements explicitly reported onto the Italian Law 326.

It has been tested and ratified by the most important gaming machine CPU boards manufacturers, either in Italy and abroad, so that no doubts can be cast about the full compatibility of the RM5 CCTalk coin mechanism with the most commonly used model of gaming board now a day available on the market.

It is fit with two different connectors as per the CCTalk standard specifications so to satisfy the wider connecting necessities and it can drive coin separators ranging from 2 to 4 plus rejection coin way out

We report hereunder the technical data and the relevant polls implemented onto the coin mechanism.

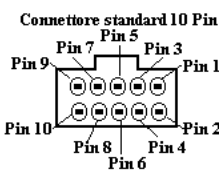
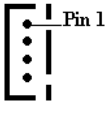
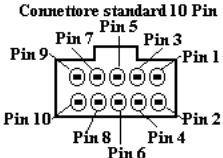
<i>Power supply:</i> 12÷24 VDC ±10%	<i>Dimensions:</i> 102x89x52 mm
<i>Coin channels:</i> 16 – all the euro denomination coins ranging from 0.01cents to 2Euro.	<i>Weight:</i> 185 gr.
<i>Max coin accepting speed:</i> 3 coin/sec.	<i>Operating temperature range:</i> Between 0 to 55° - from 10% to 75% condensed humidity.
<i>Coin dameter range:</i> From 16 to 31.5 mm	
<i>Interface:</i> CCTALK serial standard	

IMPLEMENTED POLLS:

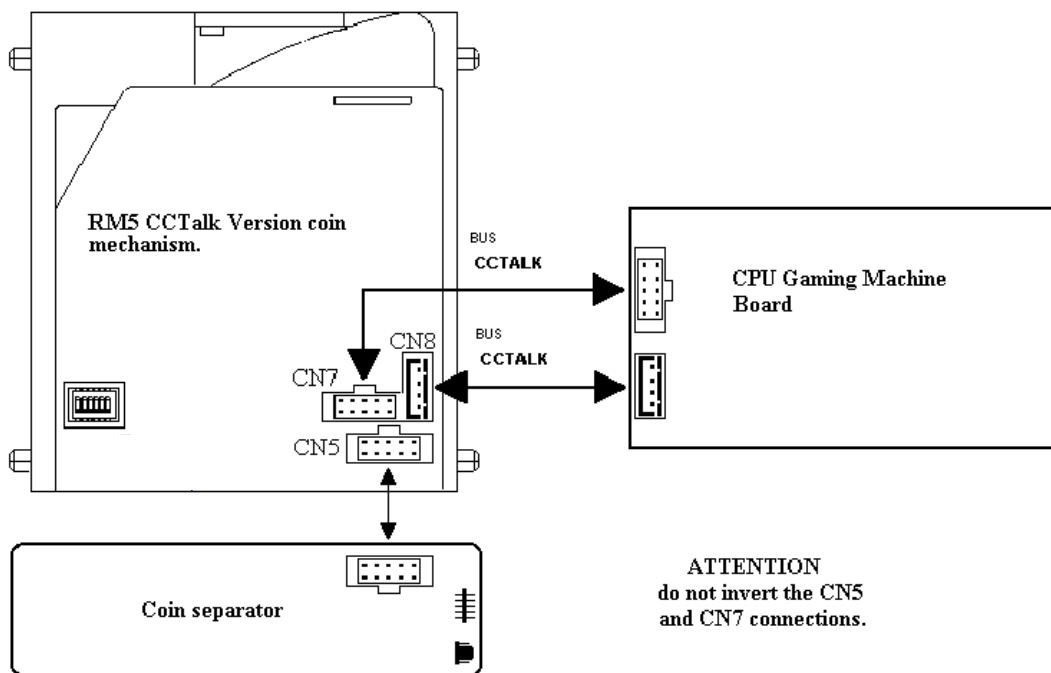
<i>Poll</i>	<i>Function</i>	<i>Poll</i>	<i>Function</i>
254	Simple poll	232	Perform self check
253	Address poll	231	Modify inhibit status
252	Address clash	230	Request inhibit status
251	Address change	229	Request Buffered credit or error codes
250	Address random	227	Request master inhibit status
249	Request polling priority	210	Modify sorter path
248	Request status	209	Request sorter path
246	Request manufacturer id	197	Calculate rom checksum
245	Request equipment category id	196	Request creation date
244	Request product code	195	Request last modification date
243	Request database version	192	Request build code
242	Request serial number	184	Request coin id
241	Request software version	170	Request base year
240	Test solenoids	169	Request address mode
238	Test output lines	4	Request comms revision
237	Read input lines	3	Clear comms status variables
236	Read opto states	2	Request comms status variables
233	Latches output lines	1	Reset Device



Connecting RM5 CC TALK to The Coin Separator (SPS31LCC3-SPS31SCC3-SPS300CC3) and to the machine CPU.

Connector	CC TALK 4 pin (CN8) connector Mod. B 4B-XH-A (JST)	CCTalk 10 Pin (CN7) Connector (CN7)																																																
 <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Pin 1</td><td>Gnd</td></tr> <tr><td>Pin 2</td><td>+12÷24 Vdc</td></tr> <tr><td>Pin 3</td><td>CH 5</td></tr> <tr><td>Pin 4</td><td>CH 6</td></tr> <tr><td>Pin 5</td><td>Not Used</td></tr> <tr><td>Pin 6</td><td>Inhibit</td></tr> <tr><td>Pin 7</td><td>CH 1</td></tr> <tr><td>Pin 8</td><td>CH 2</td></tr> <tr><td>Pin 9</td><td>CH 3</td></tr> <tr><td>Pin 10</td><td>CH 4</td></tr> </table>	Pin 1	Gnd	Pin 2	+12÷24 Vdc	Pin 3	CH 5	Pin 4	CH 6	Pin 5	Not Used	Pin 6	Inhibit	Pin 7	CH 1	Pin 8	CH 2	Pin 9	CH 3	Pin 10	CH 4	 <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Pin 1</td><td>+12÷24 Vdc</td></tr> <tr><td>Pin 2</td><td>N.U.</td></tr> <tr><td>Pin 3</td><td>0 V</td></tr> <tr><td>Pin 4</td><td>Data</td></tr> </table>	Pin 1	+12÷24 Vdc	Pin 2	N.U.	Pin 3	0 V	Pin 4	Data	 <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Pin 1</td><td>Data</td></tr> <tr><td>Pin 2</td><td>N. U.</td></tr> <tr><td>Pin 3</td><td>N.U.</td></tr> <tr><td>Pin 4</td><td>N.U.</td></tr> <tr><td>Pin 5</td><td>N.U.</td></tr> <tr><td>Pin 6</td><td>N.U.</td></tr> <tr><td>Pin 7</td><td>+ V</td></tr> <tr><td>Pin 8</td><td>0 V</td></tr> <tr><td>Pin 9</td><td>N.U.</td></tr> <tr><td>Pin 10</td><td>N.U.</td></tr> </table>	Pin 1	Data	Pin 2	N. U.	Pin 3	N.U.	Pin 4	N.U.	Pin 5	N.U.	Pin 6	N.U.	Pin 7	+ V	Pin 8	0 V	Pin 9	N.U.	Pin 10	N.U.
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Connecting Scheme



ATTENTION
 do not invert the CN5
 and CN7 connections.

Used Connectors :

- CN5: Parallel standard 10 Pin connector
- CN7: 10 Pin CCTALK Connector
- CN8: 4 Pin CCTalk Connector